



### **Advanced Micro Devices, Inc.**

### Fred Weber, Corporate VP and Chief Technology Officer

December 8, 2004

#### **Cautionary Statement**

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This presentation may contain forward-looking statements, which are made pursuant to the safe harbor provisions of the Private Securities Litigation Reform Act of 1995. Forward-looking statements involve risks and uncertainties that could cause actual results to differ materially from current expectations. Risks include the possibility that global business and economic conditions will worsen in 2005 and beyond resulting in lower than currently expected sales; that Intel Corporation's pricing, marketing programs, product bundling, new product introductions or other activities targeting the company's processor business will prevent attainment of the company's current sales plans; that demand for personal computers and, in turn, demand for the company's processors will be lower than currently expected; that adoption of AMD64 products by **OEMs** will not occur as expected; that demand for the company's Flash memory products will be lower than currently expected, particularly in the high-end mobile telephone sector and that the company will not be able to increase Flash memory market share; that Intel Corporation will negatively affect NOR Flash memory prices; that customer acceptance of MirrorBit<sup>TM</sup> technology will not continue to increase; that the company will not be able to meet demand for its products; that the company may not achieve its current product and technology introduction schedules; that the company will not be able to raise sufficient capital to enable it to establish leading-edge capacity to maintain its market leadership positions; that the company may not be able to penetrate further into emerging markets; and that solution providers will not timely provide the infrastructure, including operating systems and applications, to support the company's AMD64 technology.

Because the company's actual results may differ materially from its plans and expectations, we encourage you to review the company's filings with the Securities and Exchange Commission including, but not limited to, the Annual Report on Form 10-K for the year ended December 28, 2003, and the Quarterly Report on Form 10-Q for the quarter ended September 26, 2004. **Delivering on the AMD Vision** 



# Leading the industry to pervasive 64-bit computing



### **Core attributes:**

### End-customer benefits:

- Performance
- Security & Dependability
- Investment Protection

- Do what you want, when you want
- Enhanced Virus Protection, a trusted participant in the e-commerce community
- Simultaneous 32/64-bit x86-based computing, ready for today's and tomorrow's software

### AMD Microprocessors: Focused on Our Customers' Most Relevant Opportunities





Area represents priorities and focus

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Ultra-low-power x86 processors and AMD64 enable **x86 everywhere** 

- Simplicity for both commercial and consumer
- Expandability to accelerate innovation
- New opportunities for AMD



### **AMD Commercial Evolution**

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### Establish customer-centric leadership by delivering technology solutions that reduce complexity,

*increase the value* of our customers' and partners' offerings and change the economics in the commercial market



### **Focus on Server Solutions**

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- invent
- ProLiant DL145
- ProLiant DL585



- eServer 325
- IntelliStation A Pro workstation



- Sun Fire™ V20z
- 4P Sun Fire<sup>™</sup> V40z
- 1P Sun Java™
  Workstation W1100z
- 2P Sun Java™ Workstation W2100z











### AMD Commercial Strategy Server/Workstation Product Strategy

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#### Leverage Direct Connect Architecture and add enterprise features

Multi-core Virtualization Reliability Availability Scalability (RAS)

### **Broad product line**

Blades through large symmetric multiprocessing (SMP)

# Lead in performance/per watt/per dollar

Sell AMD Opteron<sup>™</sup> processors into non-traditional server markets

NAS, SAN, network appliances, Telco



### **Superiority of the AMD Direct Connect Architecture**

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Direct Connect Architecture helps eliminate the bottlenecks inherent in a front-side bus by directly connecting the processors, the memory controller and the I/O to the central processor unit to enable improved overall system performance and efficiency Leading the Industry to 64-bit, x86 Multi-Core

#### We Promised it First

October 1999 AMD announces multi-core-enabled processor design at Fall Microprocessor Forum.

"AMD plans to deploy multiple x86-64 processors on a single die."

#### We're Delivering it First

June 2004	AMD announces it completed dual-core AMD64 design
August 2004	AMD demonstrates industry's first x86, dual-core processor on a shipping platform
Mid 2005	AMD expects to be first to introduce dual-core processors for the one- to eight-socket server and workstation market

AMD

### Not All Software is Created Equal Remember Amdahl's Law

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### Mid-2005: AMD Opteron<sup>™</sup> Processor Dual-Core

## 

#### **90nm Process**

Approximately same die size as 130nm single-core AMD Opteron<sup>™</sup> processor\*

~205 million transistors\*

### **95 Watt Power Envelope**

Fits into 90nm power infrastructure

#### 940 Socket Compatible

All that's needed is a BIOS upgrade Compatible with all motherboards designed to our 90nm specification

\*Based on current revisions of the design 12/10/2004



### 90nm Technology is on the Mark!

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Key enabler for: • Low-power AMD64 processors • As well as high-performance offerings

### **Builds on 130nm SOI success:**

- Industry-leading SOI performance
- Industry-leading manufacturing

# AMD's 90nm technology features:

- Industry-leading SOI transistors
  - ✓ High performance
  - ✓ Significant power reduction
  - ✓ Tuned for optimum product performance
- High-performance low-k interconnect

### High yield at initial production

- AMD's Automated Precision Manufacturing (APM) technology
- Disciplined technology transition strategy

### **Revenue shipments in Q304**

### On Plan, on Time, on Budget

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November 2003 - Groundbreaking
 February 2004 - EU approval
 May 2004 - "Topping Off" ceremony
 December 2004 - Equipment move-in commences
 Mid-2005 - Process installation scheduled
 1H 2006 - Production shipments planned

### The Road Ahead

#### **More Cores**

2, 4, 8

### **New Memory Support**

DDR2, DDR3, FBDIMM

#### **Faster Input/Output**

HyperTransport<sup>™</sup>3 technology, PCI Express Gen 2

#### **Better Power Management**

Split power planes allow CPU core voltage to be reduced, while NB services probes, memory requests

### **Trusted Computing**

Enhanced Virus Protection, Presidio









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